

HVD327C

REJ03G0218-0300 Rev.3.00 May 06, 2010

Variable Capacitance Diode for VHF tuner

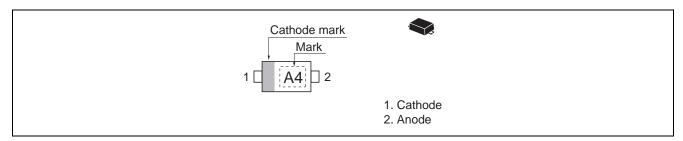
Features

- Low voltage type (tuning voltage 1 to 10V), it is suitable for ET without DC/DC converter.
- High capacitance ratio (n = 11.0 min).
- Low series resistance and good C-V linearity.
- Super small Flat Lead Package (SFP) is suitable for surface mount design.

Ordering Information

Part No	Laser Mark	Package Name	Package Code	Taping Abbreviation (Quantity)
HVD327C KRU	A4	SFP	PUSF0002ZB-A	KRU (8,000pcs / reel)

Pin Arrangement



Absolute Maximum Ratings

 $(Ta = 25^{\circ}C)$

Item	Symbol	Value	Unit
Reverse voltage	V _R	15	V
Junction temperature	Тј	125	°C
Storage temperature	temperature Tstg		°C

Electrical Characteristics

 $(Ta = 25^{\circ}C)$

Item	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse current	I _{R1}			10	nA	V _R = 10 V
	I _{R2}	_	_	100		V _R = 10 V, Ta = 60°C
Capacitance	C ₁	30.5	_	33.5	pF	V _R = 1 V, f = 1 MHz
	C ₁₀	2.6	_	2.9		V _R = 10 V, f = 1 MHz
Capacitance ratio	n	11.0	_	_	_	C ₁ /C ₁₀
Series resistance	r _S	_	_	0.8	Ω	V _R = 5 V, f = 470 MHz
Matching error	ΔC/C *1		_	2.0	%	V _R = 1 to 10 V, f = 1 MHz

Notes: 1. C.C system (Continuous Connected taping system) enable to make any 10 pcs of Δ C/C continuous in a reel, expect extention to another group.

Calculate Matching Error,

$$\Delta C/C = \frac{(Cmax - Cmin)}{Cmin} \times 100 \text{ (\%)}$$

2. For SFP package, the material of lead is exposed for cutting plane. There for, soldering nature of lead tip part is considered as unquestioned. Please kindly consider soldering nature.

Main Characteristics

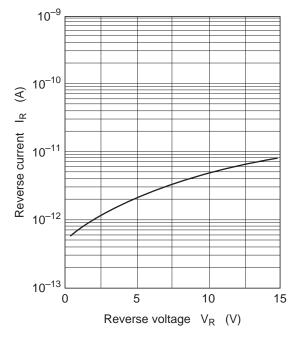


Fig.1 Reverse current vs. Reverse voltage

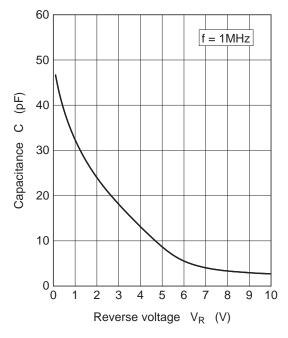
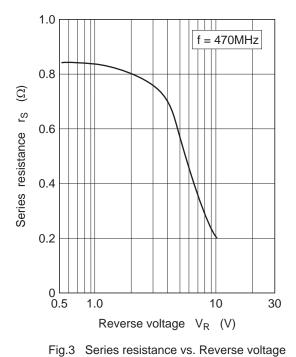
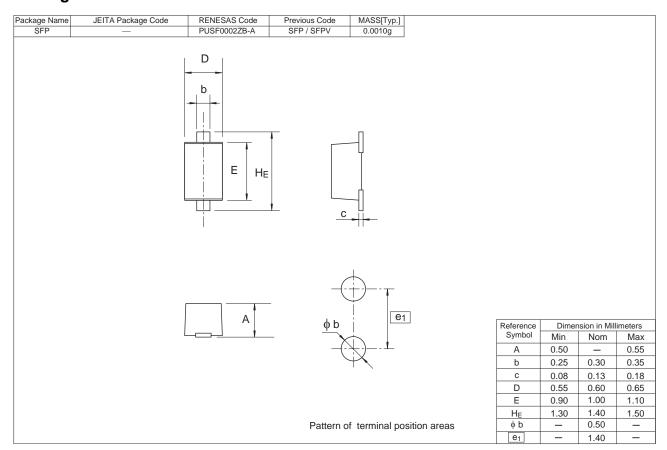


Fig.2 Capacitance vs. Reverse voltage



Package Dimensions



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